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<u>L17</u>	l9 and l15	4	<u>L17</u>
<u>L16</u>	l1 and L15	4	<u>L16</u>
<u>L15</u>	l12 and L14	163	<u>L15</u>
<u>L14</u>	(ibm or (international adj business adj machine)).as.	116718	<u>L14</u>
<u>L13</u>	l11 and L12	0	<u>L13</u>
<u>L12</u>	rad or (resource adj address adj designat\$)	53440	<u>L12</u>
<u>L11</u>	L10 not l3	8	<u>L11</u>
<u>L10</u>	l5 and L9	12	<u>L10</u>
<u>L9</u>	l6 near2 L8	151	<u>L9</u>
<u>L8</u>	adapter or adaptor	280119	<u>L8</u>
<u>L7</u>	l5 and L6	69	<u>L7</u>
<u>L6</u>	address adj space	33189	<u>L6</u>
<u>L5</u>	l1 and L4	94	<u>L5</u>

<u>L4</u>	infiniband	2641	<u>L4</u>
<u>L3</u>	l1 and L2	7	<u>L3</u>
<u>L2</u>	z/architecture or (z adj architecture)	113	<u>L2</u>
<u>L1</u>	(mmio or (memory adj mapped adj (io or "i/o" or (input adj output) or "input/output")))	2165	<u>L1</u>

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L2 and L5	20

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<u>L6</u>	l2 and l5	20	<u>L6</u>
<u>L5</u>	mmio or (memory adj mapped adj (io or "i/o" or (input adj output) or "input/output"))	2165	<u>L5</u>
<u>L4</u>	6598144.pn.	2	<u>L4</u>
<u>L3</u>	(address adj space)	33189	<u>L3</u>
<u>L2</u>	(rad or (resource adj address adj designation))	53440	<u>L2</u>
<u>L1</u>	((rad or (resource adj address adj designation)) with (field))	619	<u>L1</u>

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IEEE JNL IEEE Journal or Magazine

IEE JNL IEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

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- ☐ 1. **Virtual BUS: a network technology for setting up distributed resources in computer**
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- ☐ 2. **A parallel processor architecture for real-time digital image processing**
 Sim, C.C.; Wong, W.C.; Ong, K.;
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- ☐ 4. **A 16-bit microprocessor-based digital filter architecture**
 Mackay, J.; Silverman, H.;
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- ☐ 5. **Exposing memory mapped I/O using NFS**
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☐ **6. A processing element for medium grain distributed computing**

Bavan, A.S.; Ramnarine, R.;

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26 Mar 1992 Page(s):3/1 - 3/4

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